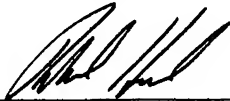




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Applicant: Pekarek *et al.*

Title: Impedance Mismatch Modeling In A Data Flow Or
Discrete Time Based System Simulation

Serial No.: 10/611,314

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- X Response to Office Action dated May 26, 2006
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